

PATENT
Docket No. HOL-001DVA

TFW
2683

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2683

Serial No.: 09/975,898

Examiner: Keith Ferguson

Filed: October 10, 2001

In re Application of: Bryan Holland

For: PORTABLE LOCATOR SYSTEM AND METHOD

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 12/28/05.
Signed Stephanie Davis
Stephanie Davis

RESPONSE TO NOTICE TO FILE CORRECTED APPLICATION PAPERS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Notice to File Corrected Application Papers dated December 2, 2005, please find enclosed herewith a copy of page 28 of the specification as filed.

In the event the Patent Office charges a fee for filing the above-noted documents, including any fees required under 37 CFR 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, the Assistant Commissioner is hereby authorized to charge or credit our Deposit Account No. 50-0612. An additional copy of this page is enclosed.

Respectfully submitted,
Sierra Patent Group, Ltd.



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half the speed of the highest clock speed, and one or more of the other elements are disabled, shutdown or otherwise provided less power by the power supply. More particularly, communication to the cellular provider 28 via the cellular modem 24 is temporarily interrupted. Normally the power delivered 5 to the cellular modem 24 is interrupted.

Various other intermediary levels may be arranged to provide various power level consumption of the power source 30. The CPU 18 carries out the operation of the managing the power level in which the locator device 12 10 operates by communicating power level signals to the various elements of the locator device 12, including the power source 30, the wireless receiver 14, and the cellular modem. Initially, during the power on stage of step 110, the CPU 18 sets the locator device to operate at the “normal” level.

15 At step 120, the CPU 18 carries out an internal check of the locator device 12. The internal check comprises steps of checking the functionality of the wireless receiver 14, the memory 20, the CPU 18, the cellular modem 24, the power source 30, and the control interface 21, among other elements. The CPU 18 also ascertains its serial number or identification number, which may 20 be preprogrammed into a circuit or like hardware device (not shown) such as a



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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/975,898	10/10/2001	Bryan Holland	HOL-001DVA	2312

7590 12/02/2005

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EXAMINER

FERGUSON, KEITH

ART UNIT

PAPER NUMBER

2683

DATE MAILED: 12/02/2005

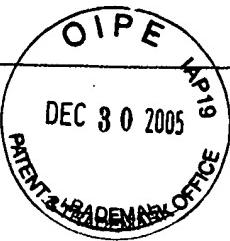
Due 1/1/05 CPL-

Please find below and/or attached an Office communication concerning this application or proceeding.





UNITED STATES PATENT AND TRADEMARK OFFICE



Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Serial Number
09975898

Date Mailed
12/2/05

NOTICE TO FILE CORRECTED APPLICATION PAPERS

Notice of Allowance Mailed

This application has been accorded an Allowance Date and is being prepared for issuance. The application, however, is incomplete for the reasons below.

Applicant is given 30 days from the mail date of this Notice within which to correct the informalities indicated below. A failure to reply will result in the application being ABANDONED. This period for reply is NOT extendable under 37 CFR 1.136 (a) or (b).

- ◆ Specification page 28, top left has missing data.

APPLICANT MUST SUPPLY MISSING INFORMATION WITHIN 30 DAYS OF THE MAIL DATE OF THIS NOTICE.

A copy of this notice MUST be returned with the reply. Please address response to Commissioner for Patents P.O. Box 1450
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Rori Burch
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